

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chatterjee et al.

Application No.: 10/788943

Filed: 2/26/2004

Title: Memory Unit with Controller Managing
Memory Access Through JTAG and CPU Interfaces

Attorney Docket No.: IDT-1872-UT

Art Unit:
2185

Examiner:
Hong C. Kim

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement mailed on September 7, 2006, Applicants hereby elect the invention(s) of group II (Claims 16-31), drawn to synchronization of data, classified in class 713, subclass 400.

Respectfully Submitted,

10/12/2006
Date

/Kenneth Glass/
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